

Electronic Acknowledgement Receipt

EFS ID:	2331507
Application Number:	09919372
International Application Number:	
Confirmation Number:	2525
Title of Invention:	Clock distribution circuits and methods of operating same that use multiple clock circuits connected by phase detector circuits to generate and synchronize local clock signals
First Named Inventor/Applicant Name:	Vadim Gutnik
Customer Number:	20792
Filer:	David Moore/Joyce Paoli
Filer Authorized By:	David Moore
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for status of Application	5347-205.pdf	56343	no	1
			001ad11a4130eca5d83c73b6207a20b4a784047e		

Warnings:

Information:		
<table border="1"><tr><td>Total Files Size (in bytes):</td><td>56343</td></tr></table>	Total Files Size (in bytes):	56343
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